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Attorney Docket No. 5646-42DVIP

**RESPONSE UNDER 37 CFR 1.116  
EXPEDITED PROCEDURE  
EXAMINING GROUP 2188**

**IN THE UNITED STATES PATENT AND TRADEMARK OFFICE**

In re: Mario Au et al.  
Serial No. 10/612,849  
Filed: July 3, 2003

Confirmation No. 6889  
Group Art Unit: 2188  
Examiner: Gary J. Portka

For: **FIFO MEMORY DEVICES HAVING MULTI-PORT CACHE MEMORY  
ARRAYS THEREIN THAT SUPPORT HIDDEN EDC LATENCY AND  
BUS MATCHING AND METHODS OF OPERATING SAME**

February 15, 2006

Mail Stop AF  
Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1451

**AMENDMENT AFTER FINAL**

Sir:

This response addresses the final Official Action of February 7, 2006.